

A display of GPU implementations in Condensed Matter Physics four distinctive cases



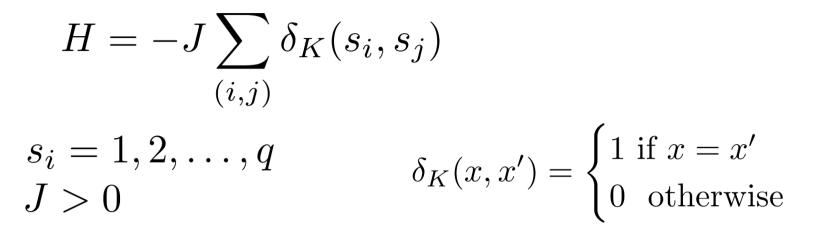
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General Purpose Graphics Processing Units have shaken up the computational scientific community. In many cases, people adopted parallelism with CUDA, before even knowing what was exactly MPI or OMP about. As statistical physicists working in condensed matter, with the aim of accelerating our simulations, we have implemented in the last few years some massively parallel codes. We present here four examples of ad-hoc models that effectively describe the phenomenology of a physical system at a given scale. We can classify them in two families: (i) lattice based Monte Carlo simulations (for classical spin models and electron glasses); (ii) overdamped dynamics of scalar systems (for elastic lines in disordered media and sheared amorphous solids). Dimensionality, interaction range, particular dynamics, and other details of the model, determine the parallelization strategy in each case.

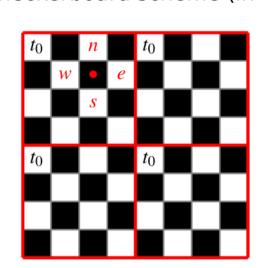
q-state Potts model Monte Carlo (classical spins system)

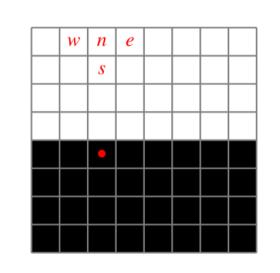


Spin flips are accepted with probability $~p= au^{-1}e^{-\Delta H/k_BT}$

Parallelization strategy

Checkerboard scheme (interactions limited to nearest neighbors)

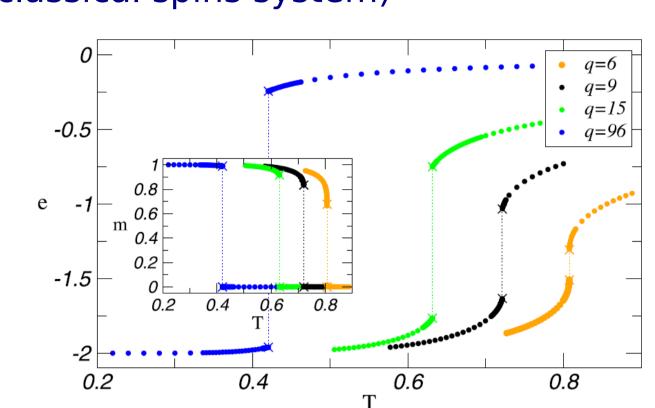




Stencil compaction-- Left: an 8×8 checkerboard framed in 4×4 (red marking), the cells updated by thread t_0 are singled out, we also marked the neighbors of cell •. Right: packed checkerboard showing first half of whites, where the neighboring cells n, e, s, w are marked, also in the second half of black cells • is singled out.

Performance

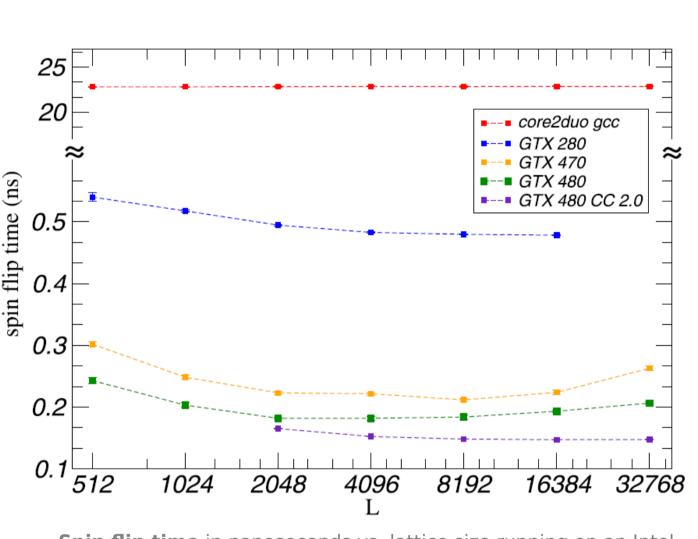
- Largely dominated by the update routine (involving 2 RNG calls per
- GTX 280 provides 42x to 47x speedup GTX 470 provides 76x to 108x speedup
- GTX 480 provides 95x to 155x speedup
- There are **two competing** factors in the loop of the update kernel: • One decreasing with L. We have one RNG for each thread, the global memory for the RNG state is retrieved one time at the beginning and stored at the end. The larger the L, the single load/store latency is distributed into more cells.
- The second factor is **increasing in L** and is given by the inherent overhead incurred by a loop (comparison and branching), that for L = 32768 amounts to 4096 repetitions.



Equilibrium energy per spin e and magnetization m (inset) versus temperature. Exact values at the transition marked as crosses.

Code features

- Pure **CUDA C**
- Implements Multiply With Carry RNG with FRAMESxFRAMES/2 independent generators.
- Computes **multiple outputs per** *thread*. Two consecutive kernels (black/white) of typically 512×512/2 threads are launched.
- Comprises the remapping of a two-dimensional stencil of four points in order to **save memory transfers**. → Encodes each spin in a byte, allowing simulations with q<256 and L^2 < available RAM. → Uses registers for RNG states.
- → Implements **parallel sums** (butterfly-like algorithm) for averages calculations using shared memory.

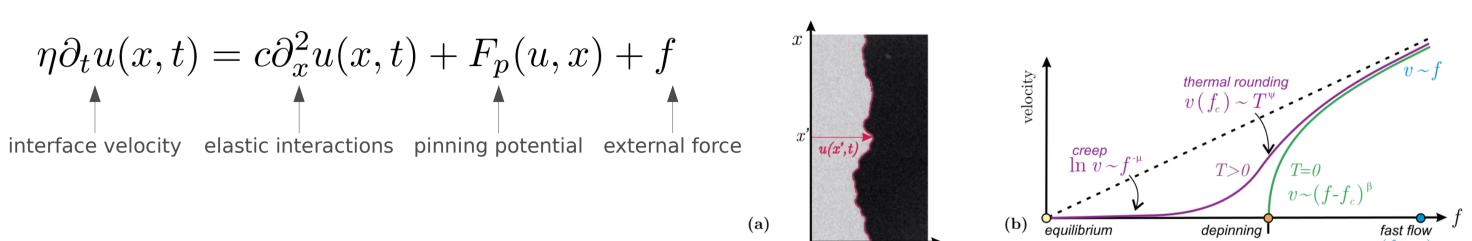


Spin flip time in nanoseconds vs. lattice size running on an Intel Core 2 Duo E8400@3.0 GHz CPU, and running on GTX 280, GTX 470 and GTX 480 NVIDIA GPUs.

[1] E.E. Ferrero, J.P. De Francesco, N. Wolovick, S. A. Cannas, Comp. Phys. Commun. 183 1578 (2012) [2] E.E. Ferrero, F. Romá, S. Bustingorry, P.M. Gleiser, *Phys. Rev. E* **86** 031121 (2012)

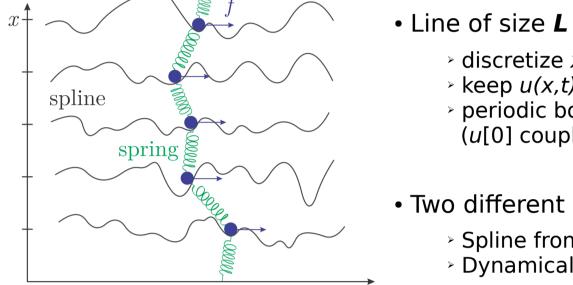
https://bitbucket.org/ezeferrero/potts https://bitbucket.org/ezeferrero/potts-glass

Quenched Edwards-Wilkinson model (elastic manifolds in disordered media)



Implementation

Euler integration in discretized time



- \rightarrow discretize x=0...(L-1)> keep u(x,t) as a real variable periodic boundary conditions (u[0] coupled with u[L-1])
- Two different disorder schemes Spline from a presorted array LxM Dynamically generated disorder

Code features

- C++ and CUDA C
- Massively parallel dynamics integration (alternating even and odd sites), L/2 threads.
- Averages of displacement velocity and quadratic width made by **Thrust** transforms and reductions.
- Coexisting CUDA kernels and Thrust functions,
- wrapping pointers. • Uses alternatively two different RNGs:
- → **MWC**, as in Potts model. → Philox, a counter based RNG of the
- Random123 library.

Uses cuFFT to compute structure factor S(q,t)

Benchmarking

Mean update time execution and their related CPU vs. CPU+GPU practical speed-ups. System size L=65536 taken as an example.

single-core CPU + GTX 470 CPU + Tesla C2075 CPU $time/ms_{l}$ |time/ms|time/msCS DP 0.396138 54.490.301181CS SP 2380.298181 ~ 54 0.227LS DP 46.46377 0.1520.123305LS SP 0.1280.101359

Platform: AMD Phenom II X4 955 Processor @3.2GHz, NVIDIA Tesla C2075, NVIDIA GTX 470.

@2.83GHz, NVIDIA GTX 480.

Case	single-core CPU	CPU + GTX 480	
	time[ms]	time[ms]	psu
CS DP	42.22	0.238	186
CS SP	~ 42	0.179	235
LS DP	36.55	0.098	373
LS SP	~ 36	0.076	477
Platform: Intel Core 2 Quad CPU Q9550			

[1] E.E. Ferrero, S. Bustingorry, A.B. Kolton, *Phys. Rev. E* **87** 032122 (2013) [2] A.B. Kolton, S. Bustingorry, E.E. Ferrero, A. Rosso, JSTAT P12004 (2013)

 $\partial_t \sigma(\mathbf{r}, t) = \mu \dot{\gamma}^{\text{ext}}_{\mathbf{A}} + \mu \int d\mathbf{r}' G(\mathbf{r}, \mathbf{r}') \dot{\gamma}^{\text{plast}}(\mathbf{r}', t)$

 $G(r,\theta) = \cos(4\theta)/\pi r^d$ $r = |\mathbf{r}' - \mathbf{r}|$

(Memcpy, Memset, Kernels)

(Kernel / Thrust call)

(Kernel / Thrust call)

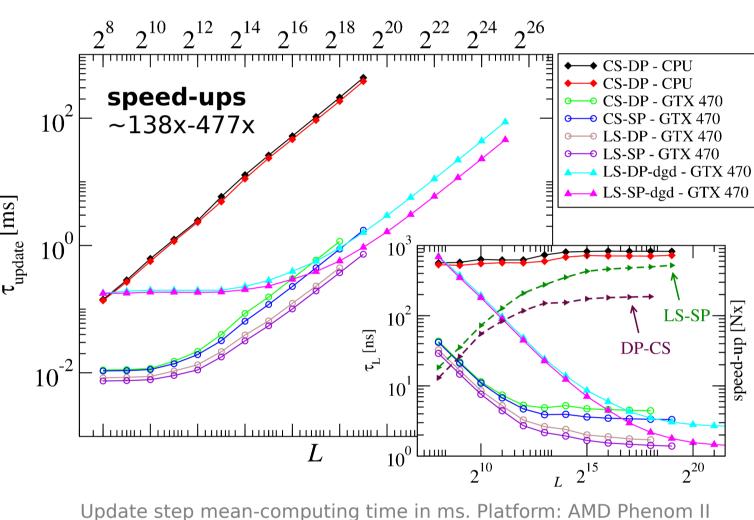
(Kernel / Thrust call)

(Thrust calls, Memcpy)

(cuFFT call)

(cuFFT call)

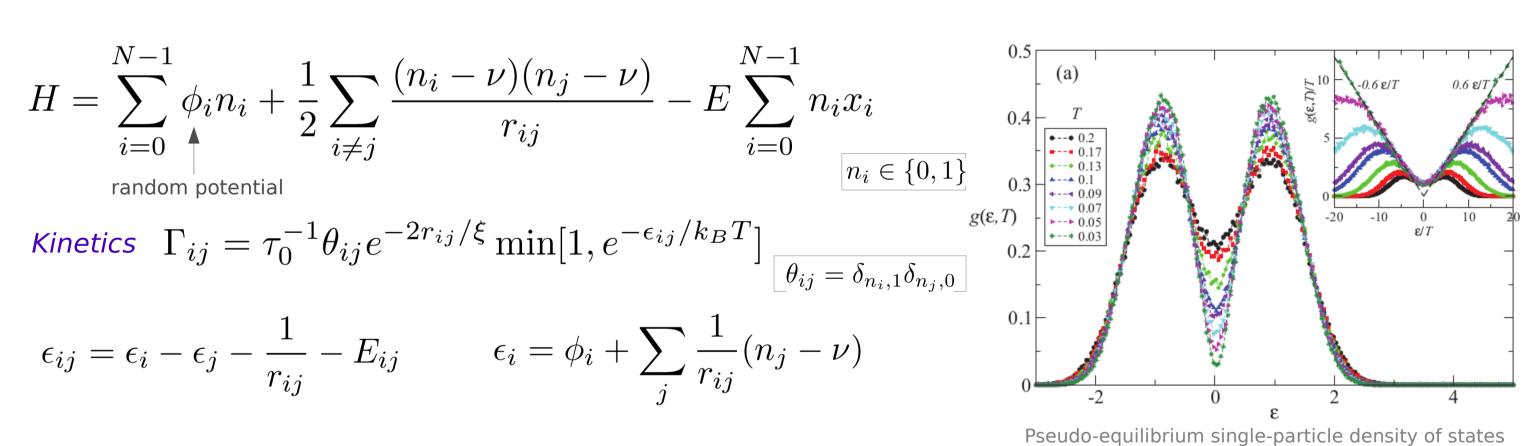
Acronyms: CS: Cubic Spline, LS: Linear Spline, DP: Double Precision, SP: Single Precision, psu: practical speed-up.



X4 955 Processor @3.2GHz, NVIDIA GTX 470

https://bitbucket.org/ezeferrero/gew

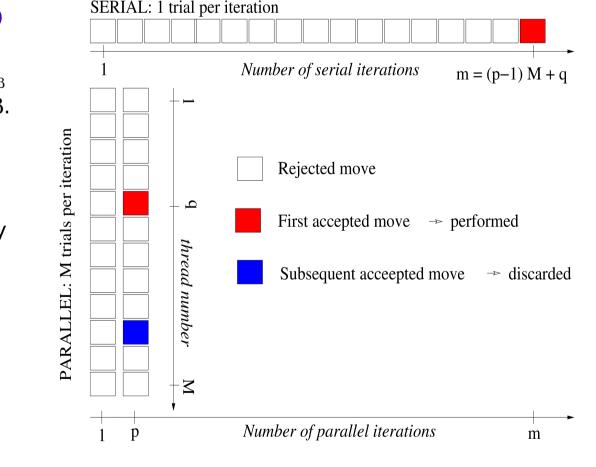
Coulomb glass Kinetic Monte Carlo (electrons glass system)



Parallel Kinetic Monte Carlo

Markov-chain MC with proposal matrix $Q_{\alpha\beta}$ and acceptance $P_{\alpha\beta}$ between conf. α and β .

- 1. Propose with independent threads (k=1,...,M), moves $\alpha \rightarrow \beta_k$.
- 2. Accept the move $\alpha \rightarrow \beta_k$ independently with prob $P_{\alpha\beta_k}$ (without updating)
- 3. If at least one thread has accepted a move, update the conf. to β_a , where qis the lowest label among the threads accepting.



Code features

- Pure CUDA C
- Trial hop $(i \rightarrow j)$: **tower sampling** with vectorized binary search.
- Metropolis: Parallel rejection
- Update Local Energies: embarrassingly parallel kernel.
- Global observables: parallel
- reductions.

Parallelization strategy

Stress change shear-rate

General work-flow

Initialization

Compute $n(\mathbf{r}), \overline{\dot{\gamma}^{\text{plast}}(\mathbf{r})}$

Fourier transf. $\overline{\dot{\gamma}^{\mathtt{plast}}(\mathbf{r})}$

Compute $G_{\mathbf{q}}\tilde{n}(\mathbf{q})\tilde{\sigma}(\mathbf{q})$

Fourier invert $G_{\mathbf{q}}\tilde{n}(\mathbf{q})\tilde{\sigma}(\mathbf{q})$

Euler integration $\partial_t \sigma$

measure?

Elastic propagator

Long-range!!

"Pseudo-spectral" method: transform Fourier, compute and anti-transform at each step

Plastic strain rate $\dot{\gamma}^{\mathrm{plast}}(\mathbf{r},t) = n(\mathbf{r},t) \frac{\sigma(\mathbf{r},t)}{c}$

$$\int d\mathbf{r}' G(|\mathbf{r} - \mathbf{r}'|) n(\mathbf{r}') \sigma(\mathbf{r}') \longrightarrow G_{\mathbf{q}} \tilde{n}(\mathbf{q}) \tilde{\sigma}(\mathbf{q})$$

$$G(r, \theta) = \cos(4\theta) / \pi r^d \longrightarrow \hat{G} = -4 \frac{q_x q_y}{q^2}$$

Embarrassingly **parallel in Fourier** space, local in (q_x, q_y)

Code features

Critical flow-curve, stability and avalanche distributions

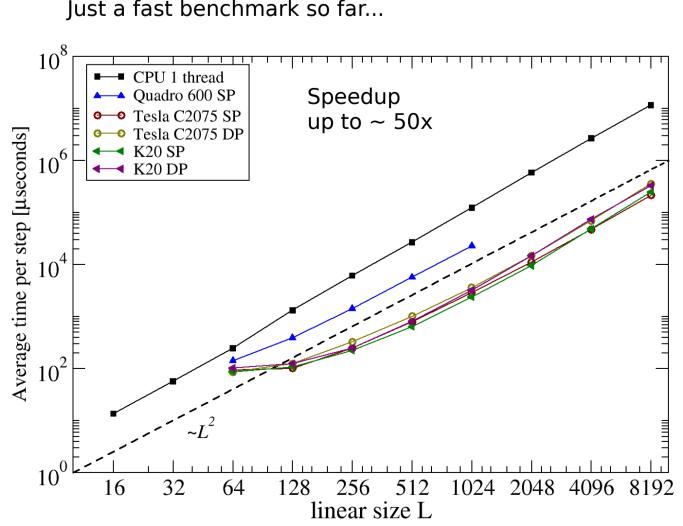
C++ and CUDA C

Athermal elasto-plastic model (sheared amorphous solids)

- Intensive use of cuFFT
- Uses Thrust for averages and extrema finding
- Uses Philox RNG

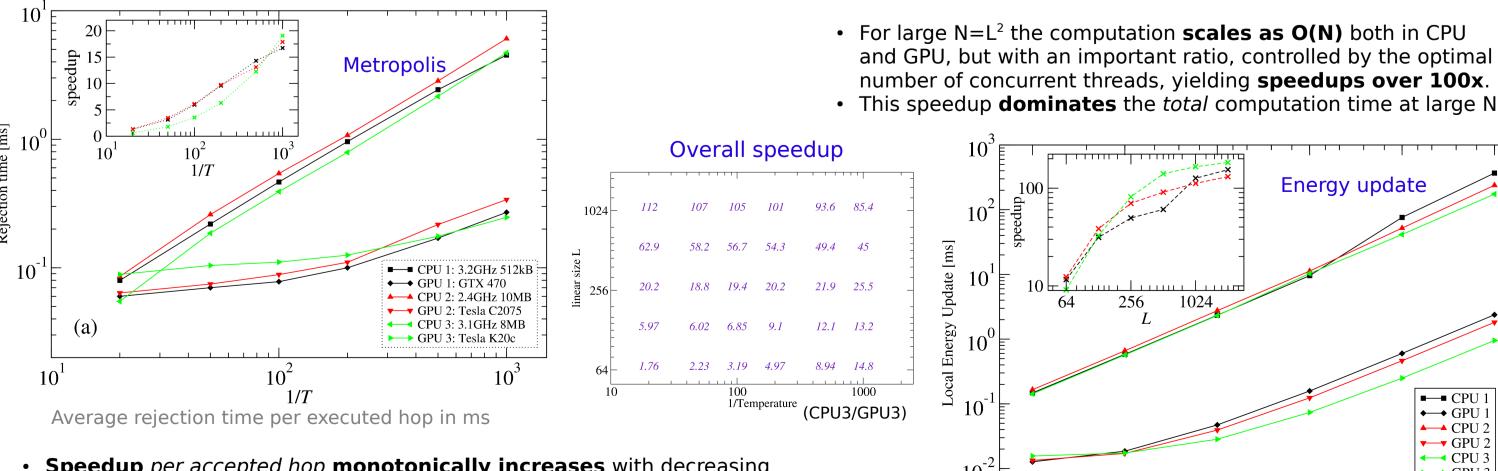
Performance

Just a fast benchmark so far...



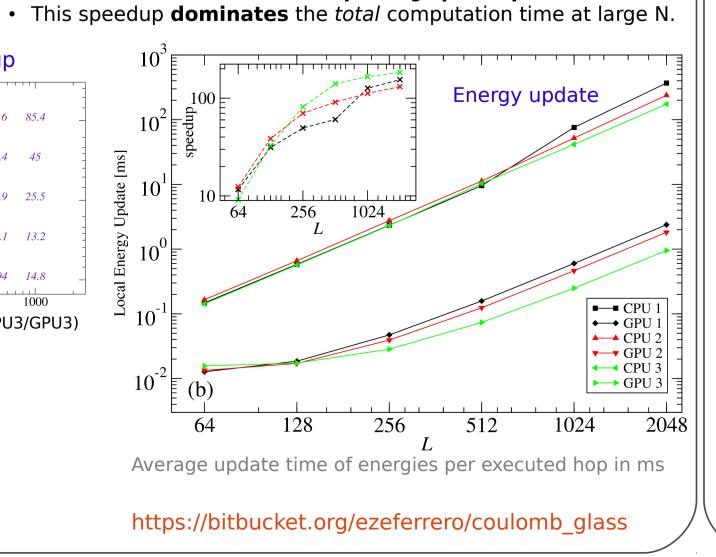
Platforms: - Intel Xeon E5-2609 @2.4GHz 10M, Quadro 600 & Tesla c2075 - Intel Sandy Bridge EP E5-2670 @2.6GHz 20M, NVIDIA K20

Performance



- Speedup per accepted hop monotonically increases with decreasing
- temperature (acceptance), roughly independent on L. • No signs of saturation for the speedup up to values as low as T=0.001.

[1] E. E. Ferrero, A. B. Kolton, M. Palassini, *AIP Conf. Proc.* **1610** 71 (2014)



[1] E. E. Ferrero, K. Martens, J.-L. Barrat, *Phys. Rev. Lett.* **113**, 248301 (2014) [2] C.Liu, E.E. Ferrero, F. Puosi, J.-L. Barrat, K. Martens, arXiv 1506.08161 (2015)

Compute quantities

https://bitbucket.org/ezeferrero/ep

General considerations:

We have adopted the the GPGPU approach to address different problems in Condensed Matter physics. The goal in each case was to implement and verify a code to produce valid physical results. Very good practical speedups have been attained respect to what people have been using before for the same kind of problems, even when optimization is not seriously considered. Present challenges include: i) exploit better the hardware, starting with CPU-GPU concurrency and multi-GPU approaches; ii) deal better with I/O operations, sometimes they drain all the obtained performance, dilemma "output raw vs compute on-the-fly"; iii) migrate from ad-hoc CUDA kernels to library calls for better portability; iv) make the jump to Open CL.